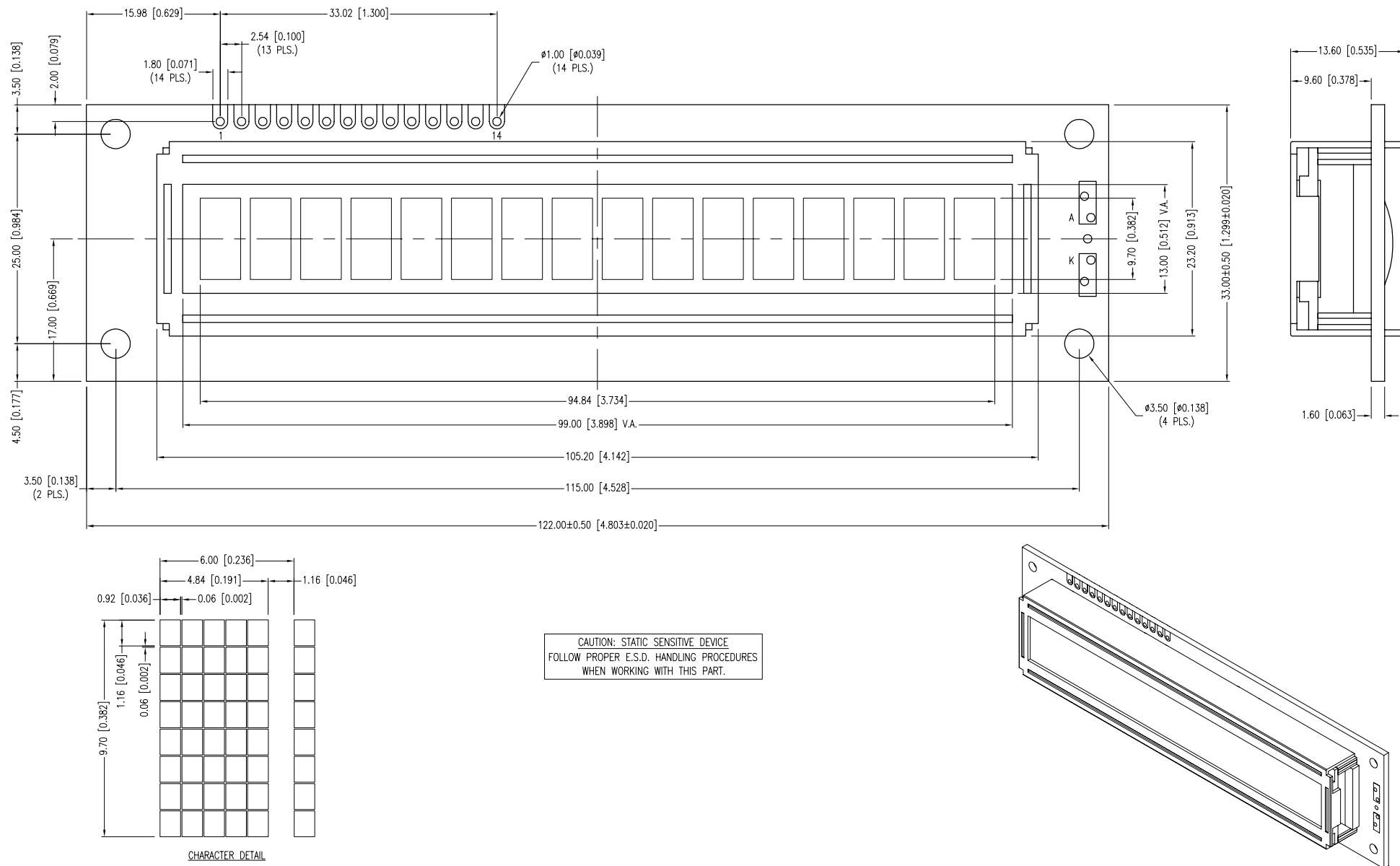




*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN.= $\begin{matrix} +\text{DECIMAL PRECISION} \\ -0.00 \end{matrix}$ MAX.= $\begin{matrix} +0.00 \\ -\text{DECIMAL PRECISION} \end{matrix}$

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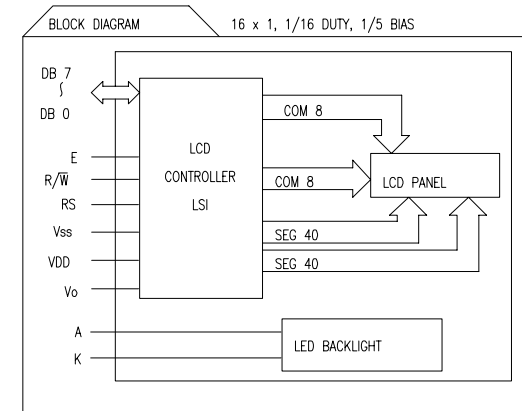
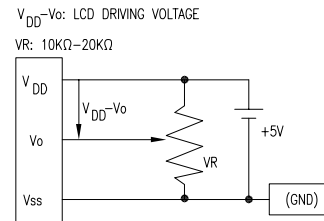
9.70mm CHAR HEIGHT, 5x8 DOT MATRIX, STN, TRANSLFECTIVE W/WHITE LED BKLT, 12:00 VIEW, 16x1 LCD MODULE.

THE SPECIFICATIONS MAY CHANGE AT ANY TIME WITHOUT NOTICE DUE TO NEW MATERIALS OR PRODUCT IMPROVEMENT.

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DATE:	07.13.11	DRAWN BY:	AB
PAGE:	1 OF 2	CHKD BY:	BC
SCALE:	NTS	APRVD BY:	KF
UNIT:	mm [INCH]		Ⓟ

PIN CONFIGURATION				
PIN NO.	SYMBOL	LEVEL	FUNCTION	
1	VSS	—	POWER SUPPLY	GND (0V)
2	VDD	—		5V
3	VO	—		FOR LCD DRIVE
4	RS	H/L	REGISTER SELECT SIGNAL H: DATA INPUT L: INSTRUCTION INPUT	
5	R/W	H/L	H: DATA READ (MODULE→MPU) L: DATA WRITE (MODULE←MPU)	
6	E	H,H→L	ENABLE	
7~14	DB0~DB7	H/L	DATA BUS—SOFTWARE SELECTABLE 4 OR 8 BIT MODE.	
15	A	—	ANODE	LED BACKLIGHT
16	K	—	CATHODE	LED BACKLIGHT



ELECTRICAL CHARACTERISTICS			VDD=4.7V to 5.3V, Ta=25°C					
ITEM			SYMBOL	CONDITION	STANDARD VALUE			UNIT
SUPPLY VOLTAGE FOR LOGIC			VDD-VSS	—	—	5.0	—	V
SUPPLY CURRENT FOR LOGIC			IDD	VDD=5V	—	1.6	2.5	mA
INPUT VOLTAGE		HIGH	VIH	—	2.2	—	VDD	V
		LOW	VIL	—	0	—	0.6	V
OUTPUT VOLTAGE		HIGH	VOH	—	2.4	—	—	V
		LOW	VOL	—	—	—	0.4	V
LED BACKLIGHT*	VOLTAGE		Vf	If=15mA	—	3.5	—	V
	CURRENT		If	—	10	15	20	mA
	POWER CONSUMPTION		PD	—	—	53	—	mW
	COLOR		—	WHITE	—	—	—	nm

*ONLY APPLIES TO MODULES WITH BACKLIGHT

ABSOLUTE MAXIMUM RATINGS					
ITEM	SYMBOL	TEST CONDITION	STANDARD VALUE		UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS	Ta=25°C	4.7	5.3	V
SUPPLY VOLTAGE FOR LCD DRIVE	VDD-VO	—	4.2 @ 50°C	4.8 @ 0°C	V
INPUT VOLTAGE	VI	Ta=25°C	VSS	VDD	V
OPERATING TEMPERATURE	Topr	LCM-H	-20	70	°C
STORAGE TEMPERATURE	Tstg	LCM-H	-30	80	°C

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			UNIT: mm [INCH]	Ⓟ