

UNCONTROLLED DOCUMENT

PART NUMBER
LCM-H24064GSN/B-C

REV.

A

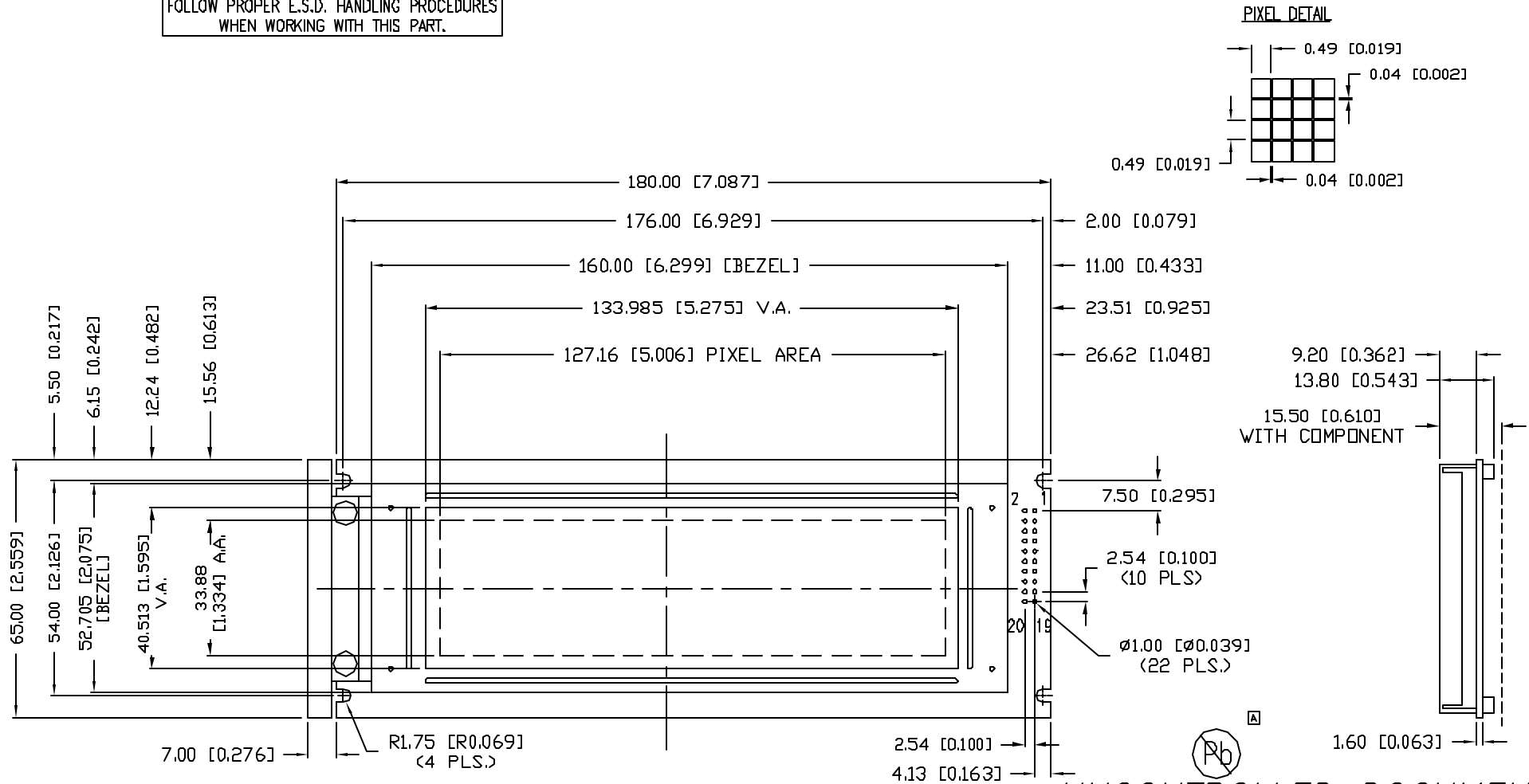
REV. E.C.N. NUMBER AND REVISION COMMENTS

DATE

A E.C.N. #11148.

4.20.07

CAUTION: STATIC SENSITIVE DEVICE
FOLLOW PROPER E.S.D. HANDLING PROCEDURES
WHEN WORKING WITH THIS PART.



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*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), XX=±0.5 (±0.020), XXX=±0.25 (±0.010), XXXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030), MIN.=+0.00 -0.00, MAX.=+0.00 -0.00

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LCM-H24064GSN/B-C

240 x 64 DOT MATRIX GRAPHIC LCD MODULE, STN BLUE,
TRANSMISSIVE, -20°C TO +70°C OP. TEMP.,
1/64 DUTY, 1/9 BIAS, CCFL BACKLIGHT.

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RELIABILITY NOTE

OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE. PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.



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APPROVED BY:

DATE: 5.24.04

PAGE: 1 OF 2

SCALE: N/A

ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
POWER SUPPLY VOLTAGE FOR LOGIC	VDD	Ta=25°C	4.5	5.0	5.5	V
POWER SUPPLY CURRENT FOR LOGIC	IDD	VDD-VSS=5V	-	16.0	25.0	mA
INPUT VOLTAGE	VIH	HIGH LEVEL	2.2	-	VDD	V
INPUT VOLTAGE	VIL	LOW LEVEL	-	-	0.6	V
OUTPUT VOLTAGE	VOH	HIGH LEVEL	2.4	-	-	V
OUTPUT VOLTAGE	VOL	LOW LEVEL	-	-	0.4	V
POWER SUPPLY VOLTAGE FOR LCD DRIVE (0°C TO 50°C)	VDD-Vo	VDD=5V Ta=25°C	12.2	13.6	14.5	V
POWER SUPPLY VOLTAGE FOR LCD DRIVE (-20°C TO +70°C)	VDD-Vo	VDD=5V Ta=25°C	12.6	14.1	14.9	V
CCFL DRIVING VOLTAGE	VCFL	Ta=25°C	-	300	-	V
CCFL FREQUENCY	fCFL	-	-	70	85	KHZ
CCFL CURRENT	ICFL	Ta=25°C	4	5	6	mA
CCFL STARTING DISCHARGE	VDCFL	Ta=5°C	(1000)	-	-	V

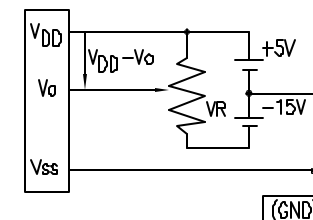
ELECTRIC MAXIMUM RATING

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
POWER SUPPLY FOR LCD DRIVE	VDD-Vo	0	-	20.0	V
POWER SUPPLY FOR LOGIC	VDD-VSS	0	-	7.0	V
INPUT VOLTAGE	VI	VSS	-	VDD	V

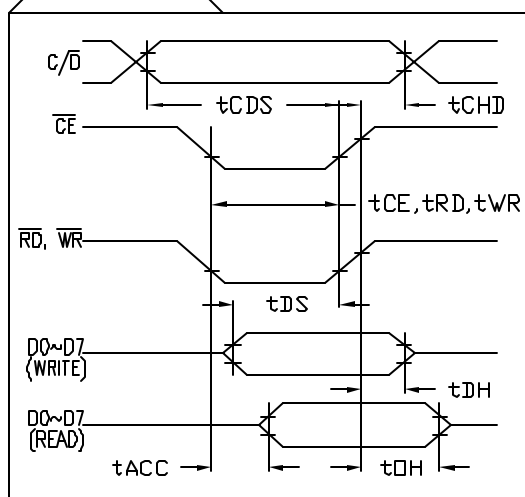
TIMING CHARACTERISTICS

PARAMETER	SYMBOL	MIN	MAX	UNIT
C/D SETUP TIME	tCDS	100	-	ns
C/D HOLD TIME	tCDH	10	-	ns
CE, RD, WR PULSE WIDTH	tCE, tRD, tWR	80	-	ns
DATA SETUP TIME	tDS	80	-	ns
DATA HOLD TIME	tDH	40	-	ns
ACCESS TIME	tACC	-	150	ns
OUTPUT HOLD TIME	tOH	10	50	ns

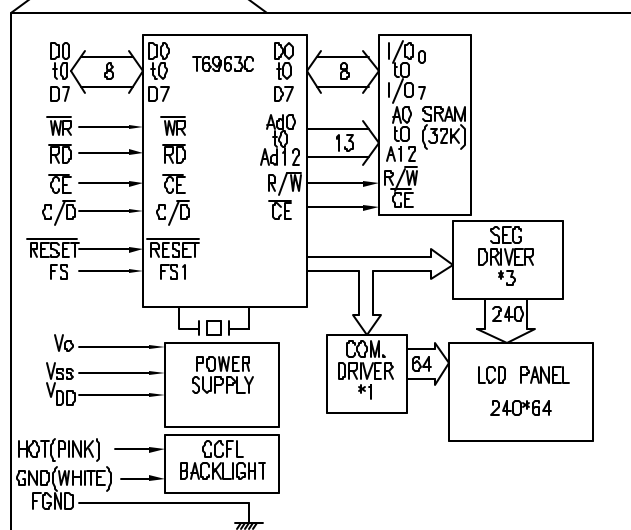
VDD-Vo: LCD DRIVING VOLTAGE
VR: 10KΩ-20KΩ



BUS TIMING



BLOCK DIAGRAM



PIN CONFIGURATION

PIN #	SYMBOL	LEVEL	FUNCTION
1	FGND	-	FRAME GROUND (CONNECTED TO METAL BEZEL)
2	GND	0V	SIGNAL GROUND
3	VDD	+5V	POWER SUPPLY
4	Vo	-	POWER SUPPLY FOR LCD DRIVE.
5	WR	L	DATA WRITE
6	RD	L	DATA READ
7	CE	L	CHIP ENABLE
8	C/D	H/L	WR="L", C/D="H": COMMAND WRITE C/D="L": DATA WRITE RD="L", C/D="H": COMMAND READ C/D="L": DATA READ
9	N/C	-	NO CONNECTION
10	RESET	L	CONTROL RESET (MODULE RESET)
11~18	DO~D7	H/L	DATA BUS
19	FS	H/L	FONT SELECT "H": 6 x 8 PIXEL/FONT "L": 8 x 8 PIXEL/FONT
20	N/C	-	NO CONNECTION

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