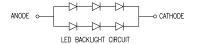


PART NUMBER	LCT-H320240M35W	REV.	В
DATE	E.C.N. NUMBER AND REVISION COMMENTS		REV.
08.15.12	E.C.N. #10BRDR/#11831.		Α
08.23.12	E.C.N. #11833.		В

В	ITFM	CONTENTS	UNIT
ы	**=***		UNII
	DISPLAY TECHNOLOGY	3.5" A-Si TFT ACTIVE-MATRIX TRANSMISSIVE	-
	MODULE OUTER DIMENSION	77.9x64.6x3.3	mm
	PIXEL SIZE	219x219	μm
	DOT PITCH	219	μm
	DOT FITCH	219	μm
	EFFECTIVE DISPLAY AREA	70.08x52.56	mm
	NUMBER OF DOTS	320 RGBx240	DOTS
	VIEWING DIRECTION	6	0'CLOCK
	COLOR FILTER ARRAY	RGB STRIPE	-
	BACKLIGHT	LED WHITE BACKLIGHT (PATENTED LED)	-
	DRIVER IC	HX8238-D	-
	INTERFACE TYPE	DIGITAL 8-BIT SERIAL/24-BIT PARALLEL RGB, CCIR601/656	-
	NUMBER OF COLORS	16.7M	-
	OPERATING TEMP.	-20~+70	•c
	STORAGE TEMP.	-30~+80	•c
	WEIGHT	33	g



1."

"SPECIAL DIMENSION(PPK/CPK DATA NEEDED>1.67)

2."**" CONTROL DIMENSION

3. ALL UNMARKED "O" OR "**" DIMENSIONS ARE REFERENCE DIMENSIONS AND NO MEASURMENT NEEDED.

4. GREEN/ROHS PRODUCT : YES

5. BACKLIGHT: WHITE

LED: 6PCS, IF=30mA , VF=10V.

6. THE SPECIFICATIONS MAY CHANGE AT ANY TIME WITHOUT NOTICE DUE TO NEW MATERIALS OR PRODUCT

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290 E. HELEN ROAD PALATINE, IL 60067-6976 PHONE: +1.847.359.2790 FAX: +1.847.359.6538 WEB: WWW.LUMEX.COM MEMBER OF ITW PHOTONICS GROUP 3.5" ACTIVE MATRIX FULL COLOR TFT PANEL 6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

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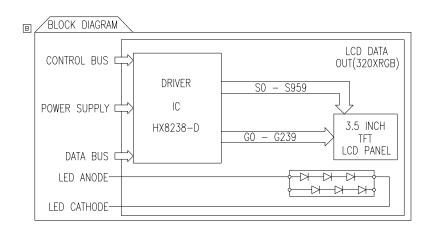
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DATE: 08.15.12 DRAWN BY: AΒ PAGE: 1 OF 9 CHKD BY: JD SCALE: NTS APRVD BY: JD UNIT: mm [INCH] (Pb)

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	_	,		
A		INITEDEACE	DIM	CONNECTION >

	KEACE FIN C	ONNECTION
PIN	SYMB0L	FUNCTION
1	A	ANODE OF BACKLIGHT POWER SUPPLY(DC 10V).
2	A	ANODE OF BACKLIGHT POWER SUPPLY(DC 10V).
3	K	CATHODE OF BACKLIGHT SUPPLY.
4	K	CATHODE OF BACKLIGHT SUPPLY.
5	VSS	GROUND.
6	VSS	GROUND.
7	QXH	DATA SEQUENCE CONTROL PIN.
8	POL	POLARITY SIGNAL TO MONITOR VCOM SIGNAL.
9	SDO	DATA OUTPUT PIN IN SERIAL MODE.
10	RESB	SYSTEM RESET PIN.
11	CSB	CHIP SELECT PIN OF SERIAL MODE.
12	SCK	CLOCK PIN OF SERIAL INTERFACE.
13	SDI	DATA PIN OF SERIAL INTERFACE.
14		DIGITAL DATA INPUT.(BLUE)
15	B0 B1	DIGITAL DATA INPUT.(BLUE)
_		. ,
16	B2	DIGITAL DATA INPUT (BLUE)
17	B3	DIGITAL DATA INPUT (BLUE)
18	B4	DIGITAL DATA INPUT (BLUE)
19	B5	DIGITAL DATA INPUT.(BLUE)
20	B6	DIGITAL DATA INPUT.(BLUE)
21	B7	DIGITAL DATA INPUT.(BLUE)
22	G0	DIGITAL DATA INPUT.(GREEN)
23	G1	DIGITAL DATA INPUT.(GREEN)
24	G2	DIGITAL DATA INPUT.(GREEN)
25	G3	DIGITAL DATA INPUT.(GREEN)
26	G4	DIGITAL DATA INPUT.(GREEN)
27	G5	DIGITAL DATA INPUT.(GREEN)
28	G6	DIGITAL DATA INPUT.(GREEN)
29	G7	DIGITAL DATA INPUT.(GREEN)
30	R0	DIGITAL DATA INPUT.(RED)
31	R1	DIGITAL DATA INPUT.(RED)
32	R2	DIGITAL DATA INPUT.(RED)
33	R3	DIGITAL DATA INPUT.(RED)
34	R4	DIGITAL DATA INPUT.(RED)
35	R5	DIGITAL DATA INPUT.(RED)
36	R6	DIGITAL DATA INPUT.(RED)
37	R7	DIGITAL DATA INPUT.(RED)
38	DEN	DISPLAY ENABLE PIN FROM CONTROLLER.
39	HSYNC	LINE SYNCHRONIZATION SIGNAL.
40	CSYNC	FRAME SYNCHRONIZATION SIGNAL.
41	DOTCLK	DOT-CLOCK SIGNAL AND OSCILLATOR SOURCE.
42	SHUT	DISPLAY SHUT DOWN PIN TO PUT THE DRIVER INTO SLEEP MODE.
43	TB	INPUT PIN TO SELECT THE GATE DRIVER SCAN DIRECTION.
44	REV	INPUT PIN TO SELECT THE DISPLAY REVISION.
45	RL	INPUT PIN TO SELECT THE SOURCE DRIVER DATA SHIFT DIRECTION.
46	CM	INPUT PIN TO SELECT 262K-COLOR OR 8-COLOR DISPLAY MODE.
47	BGR	INPUT PIN TO SELECT COLOR MAPPING.
48	VDDIO	POWER INPUT PIN.
1 10 1		
49	VCI	BOOSTER INPUT VOLTAGE



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B ELECTRICAL CHARACTERISTICS

TEM	SYMBOL	S	TANDARD VALUE	UNIT	REMARKS		
IIEM	SIMBUL	MIN	TYP	MAX	UNII	REMARKS	
	VDD	1.8	-	2.5	٧	-	
POWER VOLTAGE	VDDIO	1.4	-	3.6	٧	-	
	VIC	2.5 OR VDDIO	-	3.6	٧	=	
GATE ON VOLTAGE	VGH	-	15	-	٧	-	
GATE OFF VOLTAGE	VGL	-	-10	-	٧	-	
INPUT HIGH VOLTAGE	VIH	0.8*VDDIO	1	VDDIO	٧	-	
INPUT LOW VOLTAGE	VIL	-	ı	0.2*VDDI0	٧	-	
OUTPUT HIGH VOLTAGE	VOH	0.9*VDDIO	1	VDD	٧	IOH=100µA	
OUTPUT LOW VOLTAGE	VOL	VSS	-	0.1*VDDI0	٧	IOL=100,uA	
VCOM HIGHT OUTPUT VOLTAGE	VCOMH	2.5	3.6	4.5	٧	-	
VCOM LOW OUTPUT VOLTAGE	VCOML	-3.0	-2.4	0	٧	_	

B BACKLIGHT SPECIFICATIONS

	OTAL					
CVALIDAL	STANDARD VALUE			LINUT	REMARKS	
SIMBUL	MIN	TYP	MAX	UNII	KEMAKKS	
Vf	8.8	10	10.5	٧	If=40mA	
lfm	20	-	50	mA		
Vr	-	-	5	٧		
lr	-	-	50	mA		
Х	0.26	-	0.35	-		
Y	0.26	-	0.35	-		
Lv	2800	-	-	cd/m²	If=40mA	
Δ	80	-	-	%	MIN/MAX*100%	
LED PATENTED						
		50,0	00 HOURS			
	Ifm Vr Ir X Y	Vf 8.8 Ifm 20 Vr - Ir - X 0.26 Y 0.26 Lv 2800	MIN TYP Vf 8.8 10 Ifm 20 - Vr Ir - X 0.26 - Y 0.26 - Lv 2800 - Δ 80 - LED	MIN TYP MAX Vf 8.8 10 10.5 Ifm 20 - 50 Vr - - 5 Ir - - 50 X 0.26 - 0.35 Y 0.26 - 0.35 Lv 2800 - - Δ 80 - -	MIN TYP MAX Vf 8.8 10 10.5 V Ifm 20 - 50 mA Vr - - 50 m X 0.26 - 0.35 - Y 0.26 - 0.35 - Lv 2800 - - cd/m² Δ 80 - - % LED PATENTED	

B ABSOLUTE MAXIMUM RATINGS

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ITEM	SYMBOL	TEST CONDITION	STAN	UNIT			
IIICM	SIMBOL	TEST CONDITION	MIN	TYP	MAX	UNII	
	VDDIO	VSS=0	-0.3	-	4.0	٧	
POWER VOLTAGE	VDD	VSS=0	-0.3	-	2.7	٧	
	VCI	VSS=0	VSS-0.3	-	5.0	٧	

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UNCONTROLLED DOCUMENT

DRAWN BY:

CHKD BY:

APRVD BY:

JD

JD

3.5" ACTIVE MATRIX FULL COLOR TFT PANEL 6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP. **THE SPECIFICATIONS MAY CHANGE AT ANY TIME WITHOUT NOTICE DUE TO NEW MATERIALS OR PRODUCT IMPROVEMENT.**

PAGE: 3 OF 9 SCALE: NTS UNIT: mm [INCH]

08.15.12

DATE:

PALATINE, IL 60067-6976 PHONE: +1.847.359.2790 FAX: +1.847.359.6538 WEB: WWW.LUMEX.COM

MEMBER OF ITW PHOTONICS GROUP

290 E. HELEN ROAD

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	/	
В	✓ OPTICAL	CHARACTERISTICS

ITEM	SYMBOL	CONDITION	STAN	IDARD VALU	UNIT	NOTE	
IIEM	SIMBUL	CONDITION	MIN	TYP	MAX	UNII	NOIE
RESPONSE TIME	Tr	θ=φ=0°C	-	15	30	v	1
KESPUNSE TIME	Tf	υ-φ-υ υ	-	35	50	l '	'
CONTRAST RATIO	CR	θ=ф=0°С	200	300	-	٧	2
	RIGHT	φ=0°C	-	45	-		
VIEWING ANOLE (CD>10)	LEFT	φ =1 80°C	-	45	-	DEG	,
VIEWING ANGLE (CR≥10)	UPPER	ф=90°С	-	15	-	DEG	3
	LOWER	ф=270°С	-	35	-		
LUMINANCE OF WHITE (CENTER POINT OF LCM)	L	-	200	300	-	cd/m²	5
	Rx		0.610	0.640	0.670	-	4
	Ry		0.314	0.344	0.374	-	
COLOR CROMACITY	Gx		.264	.294	.324	-	
(CIE1931)	Gy	θ=φ=0°C	.557	.587	.617	-	"SIMULATION
"SIMULATION DATA	Bx	υ-φ-υ τ	0.102	0.132	0.162	-	REFERENCE
REFERENCE ONLY"	Ву		.106	.136	.166	-	ONLY"
	Wx		0.282	0.312	0.342	-	
	Wy		0.319	0.349	0.379	-	
OPTIMUM VIEWING DIRECTION	6 O'CLOCK					-	

NOTES:

NOTE(2): DEFINITION OF CONTRAST RATIO.

CR=BRIGHTNESS AT ALL PIXELS "WHITE" / BRIGHTNESS AT ALL PIXELS "BLACK".

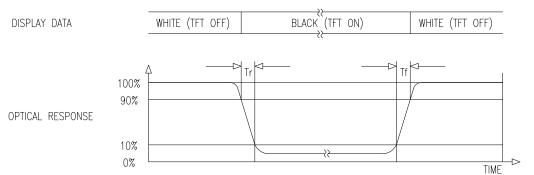
NOTE(4): MEASURED AT CENTER POINT VERTICALLY WITH BACKLIGHT ON."

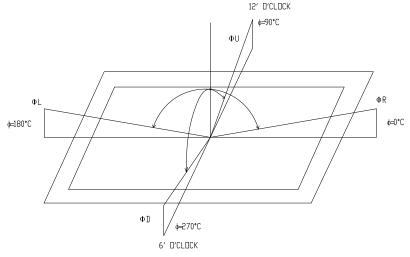
NOTE(3): DEFINITION OF VIEWING ANGLE.

NOTE(5): AFTER STABILIZING AND LEAVING THE PANEL ALONE AT GIVEN TEMPERATURE FOR 30MIN, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN STABLE, WINDLESS, AND DARK ROOM 30 MINS AFTER LIGHTING THE BACK-LIGHT. THIS SHOULD BE MEASURED IN THE CENTER OF SCREEN.

TO STABLE THE PROPERTY OF THE PARTY OF

NOTE(1): DEFINITION OF RESPONSE TIME





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 AB

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 CHKD BY:
 JD

 SCALE:
 NTS
 APRVD BY:
 JD

 UNIT:
 mm [INCH]
 Reg

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STANDARD SPECIFICATION OF RELIABILITY TEST

31711107	ARD SPECIFICATION OF RELIABILITY	ILJI		
NO.	TEST ITEM	CONTENT OF TEST	TEST CONDITION	APPLICABLE STANDARD
1	HIGH TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	80+/-3°C 240HRS	
2	LOW TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE LOW STORAGE TEMPERATURE FOR A LONG TIME.	-30+/-3°C 240HRS	
3	HIGH TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND THE THERMAL STRESS TO THE ELEMENT FOR A LONG TIME.	70+/-3°C 240HRS	
4	LOW TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS UNDER LOW TEMPERATURE FOR A LONG TIME.	-20+/-3°C 240HRS	
5	HIGH TEMPERATURE/ HUMIDITY OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND TEMPERATURE / HUMIDITY STRESS TO THE ELEMENT FOR A LONG TIME.	40°C, 90%RH 240HRS	MIL-202E-103B JIS-C5023
6	TEMPERATURE CYCLE	ENDURANCE TEST APPLYING THE LOW AND HIGH TEMPERATURE CYCLE. -30°C 25°C 80°C 30 MIN 5 MIN 30 MIN 1 CYCLE	-30°C/80°C 10 CYCLES	
	•	MECHAN	IICAL TEST	
7	DROP TEST	ENDURANCE TEST APPLYING THE DROP DURING TRANSPORTATION.	PACKED,100cm FREE FALL(6 SLIDES, 1 CORNER, 3 EDGES)	

REMARKS:

- 1. FOR OPERATION TEST, ABOVE SPECIFICATION IS APPLICABLE WHEN TEST PATTERN IS CHANGING DURING ENTIRE OPERATION TEST.
 2. INSPECTIONS AFTER RELIABILITY TESTS ARE PERFORMED WHEN THE DISPLAY TEMPERATURE RESUMES BACK TO ROOM TEMPERATURE.
 3. IT IS A NORMAL CHARACTERISTIC THAT SOME DISPLAY ABNORMALITY CAN BE SEEN DURING REABILITY TEST. IF THE DISPLAY ABNORMALITY CAN RESUME BACK TO NORMAL CONDITION AT ROOM TEMPERATURE WITHIN 24 HOURS, THERE IS NO PERMANENT DESTRUCTION OVER THE DISPLAY. THE DISPLAY STILL POSSESSES ITS FUNCTIONALITY AFTER REABILITY TESTS.

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QUALITY ASSURANCE

ACCEPTABLE QUALITY LEVEL (AQL)

EACH LOT SHOULD SATISFY THE QUALITY LEVEL DEFINED AS FOLLOWS:

A. INSPECTION METHOD: MIL-SDT-105E LEVEL II NORMAL ONE TIME SAMPLING.

B. AQL LEVEL.

CATEGORY	AQL	DEFINITION		
MAJOR	0.25%	FUNCTIONAL DEFECTIVE AS PRODUCT.		
MINOR	1.00%	SATISFY ALL FUNCTIONS AS PRODUCT BUT NOT SATISFY COSMETIC STANDARD.		

COSMETIC SCREENING CRITERIA

NO	DEFECT		JUDGEMENT CRITERIA		CATEGORY
1	SPOTS/DUST /BUBBLE (ROUND TYPE)	SIZE, D(mm) D≤0.15 0.15 <d≤0.20 D>0.20</d≤0.20 	ACCEPTABLE QU	IANTITY IN ACTIVE AREA 3 0	MINOR
2	DUST/ SCRATCHES/ BLACK STREAK (LINE TYPE)	WIDTH, W(mm) W≤0.02 W≤0.03 W≤0.05 W>0.05	LENGTH, L(mm) DISREGARD $L \le 1.0$ $L \le 2.0$ DISREGARD	ACCEPTABLE QUANTITY IN ACTIVE AREA DISREGARD DISREGARD 3 0	MINOR
3	ALLOWABLE DENSITY	ABOVE DEFECTS S EACH OTHER.	HOULD BE SEPARATE	ED MORE THAN 5mm	MINOR
4	RAINBOW	OBVIOUS UNEVEN NOTICEABLE.	OBVIOUS UNEVEN COLOR (RAINBOW) SHALL NOT BE NOTICEABLE.		MINOR
5	DISPLAY CONDITION		DIM DISPLAY ON THE PATTERNS, EXTRA PATTERN AND SHORT CIRCUIT ARE NOT ACCEPTABLE.		MAJOR
6	NO DISPLAY OR MISSING DISPLAY		THE PATTERNS OF DISPLAY SHALL LIGHT UP AS REQUIRED. NO DISPLAY OR MISSING DISPLAY ARE NOT ACCEPTABLE.		MAJOR

NOTE: D= (LONG LENGTH + SORTH LENGTH)/2

FAILURE JUDGMENT CRITERIA

AFTER RELIABILITY TEST ABOVE, TEST SAMPLE SHALL BE LET RUN TO ROOM TEMPERATURE AND HUMIDITY AT LEAST 4 HOURS BEFORE FINAL TESTS ARE CARRIED OUT.

CRITERION ITEM	FAILURE JUDGMENT CRITERIA
ELECTRICAL CHARACTERISTIC	ELECTRICAL SHORT AND OPEN.
MECHANICAL CHARACTERISTIC	OUT OF MECHANICAL SPECIFICATION.
OPTICAL CHARACTERISTIC	OUT OF APPEARANCE STANDARD.

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PRECAUTIONS FOR USING LCD MODULE

HANDLING PRECAUTIONS

- 1, THE DISPLAY PANEL IS MADE OF GLASS AND POLARIZER. DO NOT SUBJECT IT TO MECHANICAL SHOCK BY DROPPING OR IMPACT WITCH MAY CAUSE CHIPPING ESPECIALLY ON THE EDGES.
- 2. DO NOT TOUCH, PUSH OR RUB THE EXPOSED POLARIZERS WITH ANYTHING HARDER THAN AN HB PENCIL LEAD (GLASS,TWEEZERS, ETC.). THE POLARIZER COVERING THE DISPLAY SURFACE OF THE LCD MODULE IS SOFT AND EASILY SCRATCHED. HANDLE THIS POLARIZER CAERFULLY.
- 3. IF THE DISPLAY SURFACE BECOMES CONTAMINATED, BREATHE ON THE SURFACE AND GENTLY WIPE IT WITH A SOFT DRY CLOTH. IF IT IS HEAVILY CONTAMINATED, MOISTEN CLOTH WITH ISOPROPYL ALCOHOL OR ETHYL ALCOHOL. AVOID USING SOLVENTS LIKE ACETONE (KETENE), WATER, TOLUENE, ETHANOL TO CLEAN THE POLARIZER SURFACE.
- OCCUR WITH HIGH TEMPERATURE AND HIGH HUMIDITY
- 5. DO NOT APPLY EXCESSIVE FORCE TO THE DISPLAY SURFACE OR THE ADJOINING AREAS SINCE THIS MAY CAUSE THE COLOR TONE TO VARY
- 6. INSTALL THE LCD MODULE BY USING THE MOUNTING HOLES. WHEN MOUNTING THE LCD MODULE MAKE SURE IT IS FREE OF TWISTING, WARPING AND DISTORTION.
- 7. EXERCISE CARE TO MINIMIZE CORROSION OF THE ELECTRODE. CORROSION OF THE ELECTRODES IS ACCELERATED BY WATER DROPLETS, MOISTURE CONDENSATION OR
- A CURRENT FLOW IN A HIGH-HUMIDITY ENVIRONMENT.
- 8. NC TERMINAL SHOULD BE OPEN. DO NOT CONNECT ANYTHING.
- 9. IF THE LOGIC CIRCUIT POWER IS OFF, DO NOT APPLY THE INPUT SIGNALS. 10. AVOID CONTACTING OIL AND FATS.
- 11. CONDENSATION ON THE SURFACE AND CONTACT WITH TERMINALS DUE TO COLD WILL DAMAGE, STAIN OR DIRTY THE POLARIZERS. AFTER PRODUCTS ARE TESTED AT LOW TEMPERATURE THEY MUST BE WARMED UP IN A CONTAINER BEFORE COMING IN CONTACT WITH ROOM TEMPERATURE AIR.
- 12. WIPE OFF SALIVA OR WATER DROPS IMMIDEATLY, CONTACT WITH WATER OVER A LONG PERIOD OF TIME MAY CAUSE DEFORMATION OR COLOR FADING.

PRECAUTION OF SOLDERING TO THE LCM

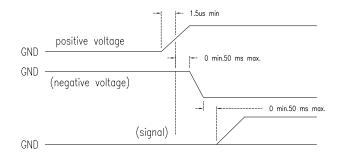
- 1, OBSERVE THE FOLLOWING WHEN SOLDERING LEAD WIRE, CONNECTOR CABLE AND ETC. TO THE LCD MODULE.
- SOLDERING IRON TEMPERATURE: 300~350°C.
- SOLDERING TIME: < 3 SEC.
- SOLDER: FUTECTIC SOLDER
- ABOVE IS A RECOMMENDED APPROACH. DUE TO DIFFERENT SOLDER COMPOSITION AND PROCESSING METHOD, IT IS RECOMMENDED THAT CUSTOMER TO STUDY AND FINE TUNING THEIR SOLDERING PROCESS PARAMETERS ACCORDINGLY.
- 2. IF SOLDERING FLUX IS USED, BE SURE TO REMOVE ANY REMANING FLUX AFTER FINISHING TO SOLDERING OPERATION. (THIS DOES NOT APPLY IN THE CASE OF A 4. PLEASE KEEP THE TEMPERATURE WITHIN SPECIFIED RANGE FOR USE AND STORAGE. POLARIZATION DEGRADATION, BUBBLE GENERATION OR POLARIZER PEEL-OFF MAY NON-HALOGEN TYPE OF FLUX.) IT IS RECOMMENDED THAT YOU PROTECT THE LCD SURFACE WITH A COVER DURING SOLDERING TO PREVENT ANY DAMAGE DUE TO FLUX SPATTERS.

ELECTRO-STATIC DISCHARGE CONTROL

- 1, SINCE THIS MODULE USES A CMOS LSI, THE SAME CAERFUL ATTENTION SHOULD BE PAID TO ELECTROSTATIC DISCHARGE AS FOR AN ORDINARY CMOS IC. 2. BE SURE TO GROUND THE BODY WHEN HANDLING THE LCD MODULES, TOOLS REQUIRED FOR ASSEMBLING, SUCH AS SOLDERING IRONS, MUST BE PROPERLY GROUNDED
- 3. TO REDUCE THE AMOUNT OF STATIC ELECTRICITY GENERATED, DO NOT CONDUCT ASSEMBLING AND OTHER WORK UNDER DRY CONDITIONS. TO REDUCE THE GENERATION OF STATIC ELECTRICITY, BE CARFUL THAT THE AIR IN THE WORK AREA IS NOT TOO DRY, A RELATIVE HUMIDITY OF 50%-60% IS RECOMMENDED. 4. THE LCD MODULE IS COATED WITH A FILM TO PROTECT THE DISPLAY SURFACE, EXERCISE CARE WHEN PEELING OFF THIS PROTECTIVE FILM SINCE STATIC FLECTRICITY MAY BE GENERATED
- 5. WHEN SOLDERING THE TERMINAL OF LCM, MAKE CERTAIN THE AC POWER SOURCE FOR THE SOLDERING IRON DOES NOT LEAK.

PRECAUTION FOR OPERATION

- 1. VIEWING ANGLE VARIES WITH THE CHANGE OF LIQUID CRYSTAL DRIVING VOLTAGE (Vo.). ADJUST VO TO SHOW THE BEST CONTRAST.
- 2. DRIVING THE LCD IN THE VOLTAGE ABOVE THE LIMIT SHORTERNS ITS LIFETIME.
- 3. RESPONSE TIME IS GREATLY DELAYED AT TEMPERATURE BELOW THE OPERATING TEMPERATURE RANGE. HOWEVER, IT WILL RECOVER WHEN IT RETURNS TO THE SPECIFIED TEMPERATURE RANGE.
- 4. IF THE DISPLAY AREA IS PUSHED HARD DURING OPERATION, THE DISPLAY WILL BECOME ABNORMAL. HOWEVER, IT WILL RETURN TO NORMAL IF IT IS TURNED OFF AND THEN BACK ON.
- 5. WHEN TURNING THE POWER ON, INPUT EACH SIGNAL AFTER THE POSITIVE/NEGATIVE VOLTAGE BECOMES STABLE (BELOW FIGURE IS A GENERAL ILLUSRATION WHERE TYPICAL VALUE DEPENDS ON INDIVIDUAL PRODUCT DESIGN).



*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN= +DECIMAL PRECISION MAX.= ±0.000 MAX.= +DECIMAL PRECISION MAX.= +DECI UNCONTROLLED DOCUMENT 3.5" ACTIVE MATRIX FULL COLOR TFT PANEL 6:00 VIEW, LED BACKLIGHT, -20°C TO +70°C OPERATING TEMP.

PALATINE, IL 60067-6976 PHONE: +1.847.359.2790 FAX: +1.847.359.6538 VER: WWW.LUMEX.COM

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DATE: 08.15.12 DRAWN BY: AΒ OF 9 PAGE: 7 CHKD BY: JD SCALE: NTS APRVD BY: JD UNIT: mm [INCH] (Pb)

290 E. HELEN ROAD Creating LED and LCD Solutions Together MEMBER OF ITW PHOTONICS GROUP

PART NUMBER	LCT-H320240M35W	REV.	В
DATE	E.C.N. NUMBER AND REVISION COMMENTS		REV.
08.15.12	E.C.N. #10BRDR/#11831.		Α
08.23.12	E.C.N. #11833.		В

RoHS COMPLIANT PRODUCT

1. CADMIUM AND CADMIUM COMPOUNDS	LESS THAN 100PPM
2. HEXAVALENT CHROMIUM COMPOUNDS	LESS THAN 1000PPM
3. LEAD AND LEAD COMPOUNDS	LESS THAN 1000PPM
4. MERCURY AND MERCURY COMPOUNDS	LESS THAN 1000PPM
POLYBROMINATED BIPHENYLS (PBBs)	LESS THAN 1000PPM
6. POLYBROMINATED DIPHENYL ETHERS (PBDEs)	LESS THAN 1000PPM

PACKAGING STANDARD

PRODUCT NO.	LCT-H320240M35W	RELEASE DATE	2009
PRODUCT NAME.	TFT MODULE	PREPARE BY:	
QUANTITY/ EACH BOX	168 PCS.	BOX MATERIAL	PAPER CARTON
OUTER CARTON BOX SIZE	465mm x 405mm x 305mm	BOX TYPE	NEW
QUANTITY/ INNER BOX QUANTITY/ OUTER BOX	12 X 7 X 2 = 168 PCS.	WEIGHT	8.6 KG

THERE ARE 12 PCS LCD PER EACH ANTI-STATIC PLASTIC PLATE. THERE ARE 7 LAYER PLASTIC PLATES PER EACH INNER CARTON BOX. THERE ARE 2 INNER CARTON BOX PER EACH OUTER CARTON BOX.

STORAGE

- 1. WHEN STORING LCDS AS SPARES FOR SOME YEARS, THE FOLLOWING PRECAUCTIONS ARE NECESSARY.
- 2. STORE THEM IN A SEALED POLYETHYLENE BAG. IF PROPERLY SEALED, THERE IS NO NEED FOR DESICCANT.
- 3. STORE THEM IN A DARK PLACE. DO NOT EXPOSE TO SUNLIGHT OR FLUORESCENT LIGHT, KEEP THE TEMPERATURE BETWEEN 0°C AND 35°C.
- 4. ENVIRONMENTAL CONDITIONS:
- 5. DO NOT LEAVE THEM FOR MORE THAN 168HRS. AT 60°C.
- 6. SHOULD NOT BE LEFT FOR MORE THAN 48HRS. AT -20°C.

SAFETY

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- 1. ITS RECOMMENDED TO CRUSH DAMAGED OR UNNECESSARY LCD INTO PIECES AND WASH THEM OFF WITH SOLVENTS SUCH AS ACETONE AND ETHANOL, WHICH SHOULD LATER BE
- 2. IF ANY LIQUID LEAKS OUT OF DAMAGED GLASS CELL AND COMES IN CONTACT WITH THE HANDS, WASH OFF THOROUGHLY WITH SOAP AND WATER.

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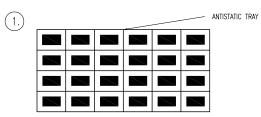
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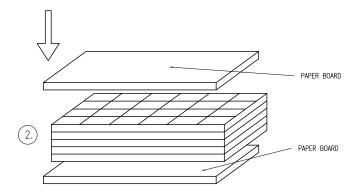
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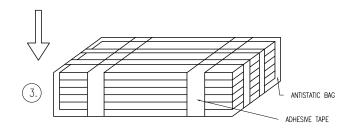
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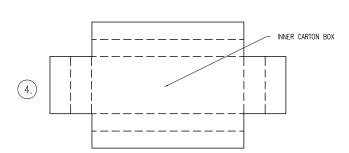
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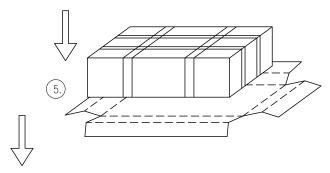
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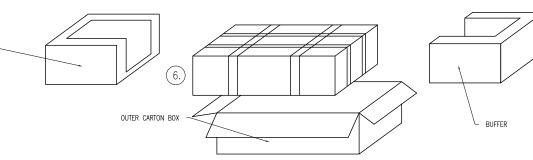












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