

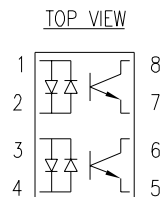
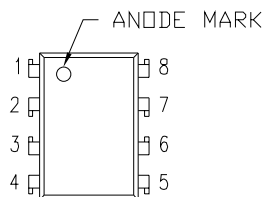
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PART NUMBER
OCP-PCT228/X

REV.

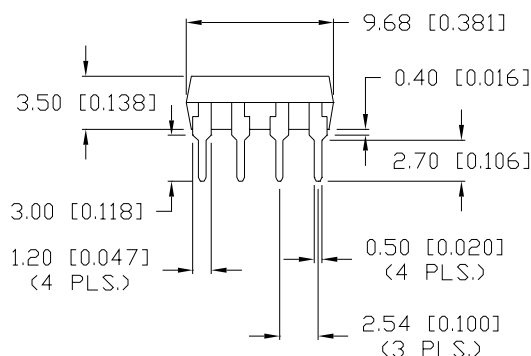
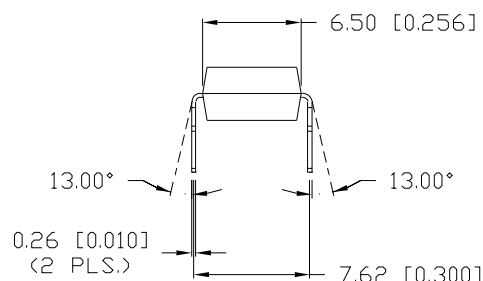
REV. E.C.N. NUMBER AND REVISION COMMENTS

DATE



NOTES:
1,3. ANODE/CATHODE
2,4. CATHODE/ANODE
5,7. EMITTER
6,8. COLLECTOR

CAUTION: STATIC SENSITIVE DEVICE
FOLLOW PROPER E.S.D. HANDLING PROCEDURES
WHEN WORKING WITH THIS PART.



PART NUMBER TABLE	
PART= /X	CTR (%)
/A	60 TO 600
/B	60 TO 300

ELECTRO-OPTICAL CHARACTERISTICS (Ta=25°C)						
	PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX UNITS
I	FORWARD VOLTAGE	V _f	I _f =±20mA	—	1.2	1.4 V
	PEAK FORWARD VOLTAGE	V _{fM}	I _{fM} =±0.5A	—	—	3.5 V
	TERMINAL CAPACITANCE	C _t	V=0, f=1kHz	—	30	— pF
O	COLLECTOR DARK CURRENT	I _{CEO}	V _{CE} =20V, I _f =0	—	—	10 ⁻⁷ A
T	CURRENT TRANSFER RATIO	CRT	I _f =±1mA, V _{CE} =5V	60	—	600 %
	COLLECTOR-EMITTER SATURATION VOLTAGE	V _{CE(sat)}	I _f =±20mA, I _c =1mA	—	0.1	0.3 V
	ISOLATION RESISTANCE	R _{iso}	DC500V	5x10 ¹⁰	10 ¹¹	— ohm
	FLOATING CAPACITANCE	C _f	V=0, f=1MHz	—	0.6	1.0 pF
	CUT-OFF FREQUENCY	f _c	V _{CE} =5V, I _c =2mA, R _L =100ohm	—	80	— kHz
	RESPONSE TIME (RISE)	t _r	V _{CE} =2V, I _c =2mA, R _L =100ohm	—	5	20 μS
	RESPONSE TIME (FALL)	t _f	V _{CE} =2V, I _c =2mA, R _L =100ohm	—	4	20 μS

I=INPUT, O=OUTPUT, T=TRANSFER CHARACTERISTICS.

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)				
	PARAMETER	SYMBOL	MAX	UNITS
I	FORWARD CURRENT	I _f	±50	mA
	PEAK FORWARD CURRENT	I _{fM}	±1	A
	POWER DISSIPATION	P _d	70	mW
O	COLLECTOR-EMITTER VOLTAGE	V _{CEO}	60	V
	EMITTER-COLLECTOR VOLTAGE	V _{ECO}	6	V
	COLLECTOR CURRENT	I _c	50	mA
	COLLECTOR POWER DISSIPATION	P _c	150	mW
	TOTAL POWER DISSIPATION	P _{tot}	200	mW
	ISOLATION VOLTAGE 1 MIN.	V _{iso}	5000	V _{RMS}
	OPERATING TEMP.	T _{opr}	- 30 TO + 100	°C
	STORAGE TEMP.	T _{stg}	- 55 TO + 125	°C
	SOLDERING TEMP.	T _{sol}	+ 260	°C
	2.0mm FROM BODY		10 SEC. MAX	

I=INPUT, O=OUTPUT.

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*UNLESS OTHERWISE SPECIFIED TOLERANCE IS ±0.25mm (±0.010")

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RELIABILITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE. PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

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EIGHT PIN DIP DUAL CHANNEL PHOTOCOUPLER, BIPOLAR INPUT,
TRANSISTOR OUTPUT WITHOUT EXTERNAL BASE CONNECTION.

DRAWN BY:	CHECKED BY:	APPROVED BY:	DATE: 9-29-99
CK/DU			PAGE: 1 OF 1
			SCALE: N/A